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	Filing Date		2006-09-01
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2434 2162	
	Examiner Name	Henning, Matthew T.	
	Attorney Docket Number	53047-57370	

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	3	09145544	JP	A	1997-06-06	Ricoh Kk		<input type="checkbox"/>
	4	11306268	JP	A	1999-11-05	Shibata Sunao et al.		<input type="checkbox"/>
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8	2001357048	JP	A	2001-12-26	Hitachi Ltd		<input type="checkbox"/>
9	2002108910	JP	A	2002-04-12	Nec Software Ltd		<input type="checkbox"/>
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	1	BRAUN ET AL., "Protocol Wrappers for Layered Network Packet Processing in Reconfigurable Hardware", IEEE Micro, Jan-Feb 2002, pp. 66-74	<input type="checkbox"/>
	2	NWODOH ET AL., "A Processing System for Real-Time Holographic Video Computation", Reconfigurable Technology: FPGAs for Computing and Application, Proceedings for the SPIE, Sept. 1999, Boston, pp. 129-140, Vol. 3844	<input type="checkbox"/>
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Attorney Docket Number	53047-57370

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